APPLICATION INFORMATION

Application Type:: Regular (Utility)

Title:: DESIGN ANALYSIS WORKSTATION FOR ANALYZING

INTEGRATED CIRCUITS

Attorney Docket Number:: 9-14774-1US-1-1

Request for Early Publication?:: No Request for Non-Publication?:: No

Suggested Drawing Figure:: 6
Total Drawing Sheets:: 8
Small Entity?:: Yes

INVENTOR INFORMATION

Inventor Authority Type:: Inventor Primary Citizenship Country:: Canada

Status:: Full Capacity

Given name:: David Middle name:: F.

Family name:: SKOLL
City of Residence:: Ottawa
State or Province of Residence:: Ontario
Country of Residence:: Canada

Street:: 986 Eiffel Avenue

City:: Ottawa
State or Province:: Ontario

Country:: Canada Postal or Zip Code:: K2C 0J2

Inventor Authority Type:: Inventor Primary Citizenship Country:: Canada

Status:: Full Capacity

Given name:: Terry
Family name:: LUDLOW
City of Residence:: Ottawa

State or Province of Residence:: Ontario
Country of Residence:: Canada

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City:: Ottawa
State or Province:: Ontario
Country:: Canada

Postal or Zip Code:: K1Y 0B1

Inventor Authority Type:: Inventor Primary Citizenship Country:: Canada

Status:: Full Capacity

Given name::

Family name::

City of Residence::

C

State or Province of Residence:: Ontario
Country of Residence:: Canada

Street:: 224 Third Avenue

City:: Ottawa
State or Province:: Ontario
Country:: Canada

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CORRESPONDENCE INFORMATION

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DOMESTIC PRIORITY INFORMATION

Application:: Continuity Type:: Parent Application:: Parent Filing Date::

09/690,813 Original N/A N/A

09/927,558 Continuation 09/690,813 October 18, 2000

ASSIGNEE INFORMATION

Assignee name:: CHIPWORKS

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